

843.38981CC4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: K. SUZUKI et al
Serial No.: (New continuation application under 37 CFR §1.53[b]
of application Serial No. 10/358,276)
Filed: August 27, 2003
Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
METHOD OF MANUFACTURING THE SAME
Group Art Unit: 2815
Examiner: J. CLARK

LETTER CLAIMING RIGHT TO FOREIGN PRIORITY

MS: PATENT APPLICATIONS

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

August 27, 2003

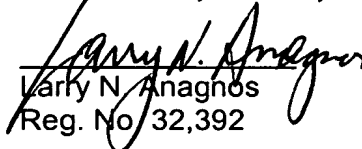
Sir:

Under the provisions of 35 USC §119 and 37 CFR §1.55, Applicants
hereby claim the right of foreign priority based on:

**JAPANESE PATENT APPLICATION NO.:
11-259460 FILED SEPTEMBER 13, 1999**

A certified copy of the priority document was filed in prior, parent
application Serial No. 09/661,372, on September 13, 2000.

Respectfully submitted,
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